

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	415873	process\$5.clm.	US-PGPUB	OR	OFF	2007/06/19 09:15
S2	59849	series.clm.	US-PGPUB	OR	OFF	2007/06/19 09:15
S3	144938	memory.clm.	US-PGPUB	OR	OFF	2007/06/19 09:15
S4	181224	input.clm.	US-PGPUB	OR	OFF	2007/06/19 09:16
S5	451574	port\$3.clm.	US-PGPUB	OR	ON	2007/06/19 09:16
S6	192199	input\$3.clm.	US-PGPUB	OR	OFF	2007/06/19 09:16
S7	154048	parallel.clm.	US-PGPUB	OR	OFF	2007/06/19 09:16
S8	209	S1 and S2 and S3 and S6 and S5 and S7	US-PGPUB	OR	OFF	2007/06/19 09:17
S9	681	unprocess\$5.clm.	US-PGPUB	OR	OFF	2007/06/19 09:17
S10	0	S8 and S9	US-PGPUB	OR	OFF	2007/06/19 09:18
S11	206423	output\$3.clm.	US-PGPUB	OR	OFF	2007/06/19 09:18
S12	183	S8 and S11	US-PGPUB	OR	OFF	2007/06/19 09:19
S13	3249	(predetermin\$5 near5 cycle\$3).clm.	US-PGPUB	OR	OFF	2007/06/19 09:23
S14	0	S12 and S13	US-PGPUB	OR	OFF	2007/06/19 09:22
S15	50498	(predetermin\$5 near5 (period\$3 or time\$2)).clm.	US-PGPUB	OR	OFF	2007/06/19 09:24
S16	28	S12 and S15	US-PGPUB	OR	OFF	2007/06/19 09:24
S17	13474	(input\$3 near3 port\$3).clm.	US-PGPUB	OR	OFF	2007/06/19 09:25
S18	1	S16 and S17	US-PGPUB	OR	OFF	2007/06/19 09:25
S19	14247	(output\$3 near3 port\$3).clm.	US-PGPUB	OR	OFF	2007/06/19 09:25
S20	204	S17 and S19 and S1 and S2	US-PGPUB	OR	OFF	2007/06/19 09:26
S21	22	S17 and S19 and S1 and S2 and S3 and S7	US-PGPUB	OR	OFF	2007/06/19 09:26
S22	0	S17 and S19 and S1 and S2 and S3 and S7 and S13	US-PGPUB	OR	OFF	2007/06/19 09:26
S23	1	S17 and S19 and S1 and S2 and S3 and S7 and S15	US-PGPUB	OR	OFF	2007/06/19 09:27
S24	56	(two and signal and processor and series and input and port).clm.	US-PGPUB	OR	OFF	2007/06/19 09:28
S25	18	(two and signal and processor and series and input and port and parallel).clm.	US-PGPUB	OR	OFF	2007/06/19 09:29
S26	8	(two and signal and processor and series and input and port and parallel and memory).clm.	US-PGPUB	OR	OFF	2007/06/19 09:29
S27	0	(two and signal and processor and series and input and port and parallel and memory and interconnected).clm.	US-PGPUB	OR	OFF	2007/06/19 09:29

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S28	2	(two and signal and processor and series and input and port and memory and interconnected).clm.	US-PGPUB	OR	OFF	2007/06/19 09:46
S29	1	(two and signal and processor and modules and series and input and port and memory and interconnected).clm.	US-PGPUB	OR	OFF	2007/06/19 09:46
S30	130	(process\$5 and two and series and memory and input and port).clm.	US-PGPUB	OR	OFF	2007/06/19 09:47
S31	48	(process\$5 and two and series and memory and input and port and mount\$5).clm.	US-PGPUB	OR	OFF	2007/06/19 09:47
S32	5	(process\$5 and two and series and memory and input and port and mount\$5 and remov\$4).clm.	US-PGPUB	OR	OFF	2007/06/19 09:48
S33	0	(process\$5 and two and series and memory and input and port and mount\$5 and remov\$4 and sitch\$4).clm.	US-PGPUB	OR	OFF	2007/06/19 09:48
S34	5	(process\$5 and two and series and memory and input and port and mount\$5 and remov\$4 and switch\$4).clm.	US-PGPUB	OR	OFF	2007/06/19 09:48
S35	3	(process\$5 and two and series and memory and input and port and mount\$5 and remov\$4 and switch\$4 and parallel).clm.	US-PGPUB	OR	OFF	2007/06/19 09:50
S36	0	(process\$5 and two and series and memory and input and port and mount\$5 and remov\$4 and switch\$4 and parallel and module\$3).clm.	US-PGPUB	OR	OFF	2007/06/19 09:50
S37	2	(process\$5 and two and series and memory and input and port and mount\$5 and remov\$4 and switch\$4 and signal and module\$3).clm.	US-PGPUB	OR	OFF	2007/06/19 09:53
S38	69	(switch\$5 and mount\$5 and two and process\$5 and series and memory).clm.	US-PGPUB	OR	OFF	2007/06/19 09:54
S39	42	(switch\$5 and mount\$5 and two and process\$5 and series and memory and input\$3 and output\$3 and port\$2).clm.	US-PGPUB	OR	ON	2007/06/19 09:54
S40	5	(switch\$5 and mount\$5 and two and process\$5 and series and memory and input\$3 and output\$3 and port\$2 and remov\$4).clm.	US-PGPUB	OR	ON	2007/06/19 09:55
S41	2	(switch\$5 and mount\$5 and two and process\$5 and series and memory and input\$3 and output\$3 and port\$2 and remov\$4 and module\$3).clm.	US-PGPUB	OR	ON	2007/06/19 09:55

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S42	2	(switch\$5 and mount\$5 and two and process\$5 and series and memory and input\$3 and output\$3 and port\$2 and remov\$4 and module\$3 and interconnect\$5).clm.	US-PGPUB	OR	ON	2007/06/19 09:55
S43	2	(switch\$5 and mount\$5 and two and process\$5 and series and memory and input\$3 and output\$3 and port\$2 and remov\$4 and module\$3 and interconnect\$5 and signal\$3).clm.	US-PGPUB	OR	ON	2007/06/19 09:56
S44	898	(Koichi near\$ Hashimoto).in.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/19 09:57
S45	21	S44 and (process\$5 and series)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/19 09:58
S46	1	S44 and (process\$5 and series and memory and module\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/19 10:01
S47	2	S44 and ((process\$5 and series).clm.)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/19 09:59
S48	1	S44 and ((process\$5 and series and memory).clm.)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/19 09:59

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S49	1	(Kazuhiro near5 Kamoshida).in.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/19 10:00
S50	11	S44 and (process\$5 and series and memory)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/19 10:01
S51	6	S44 and (process\$5 and series and memory and parallel)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/19 10:02
S52	1	S44 and (process\$5 and series and memory and parallel and unprocess\$5)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/19 10:02